

Fig. 1

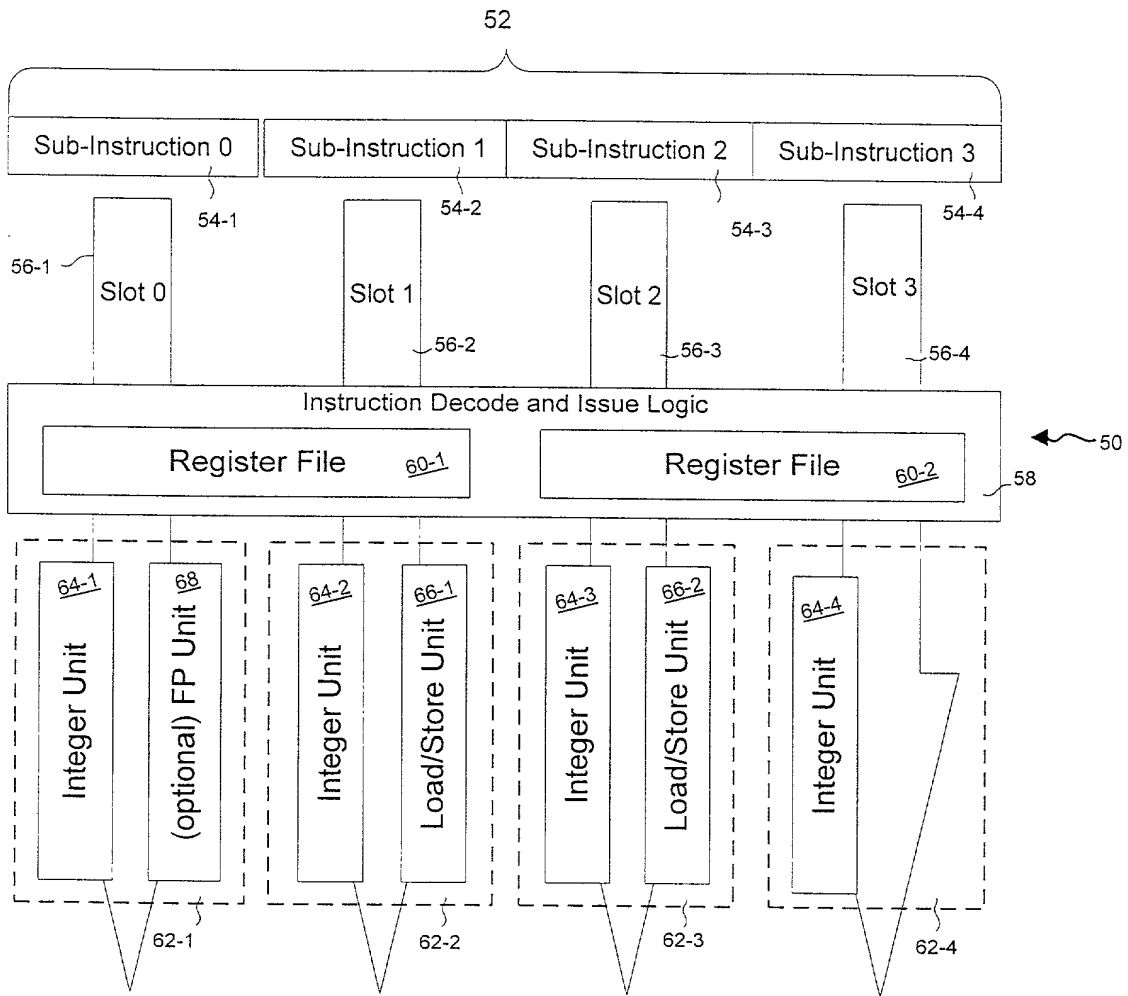


Fig. 2

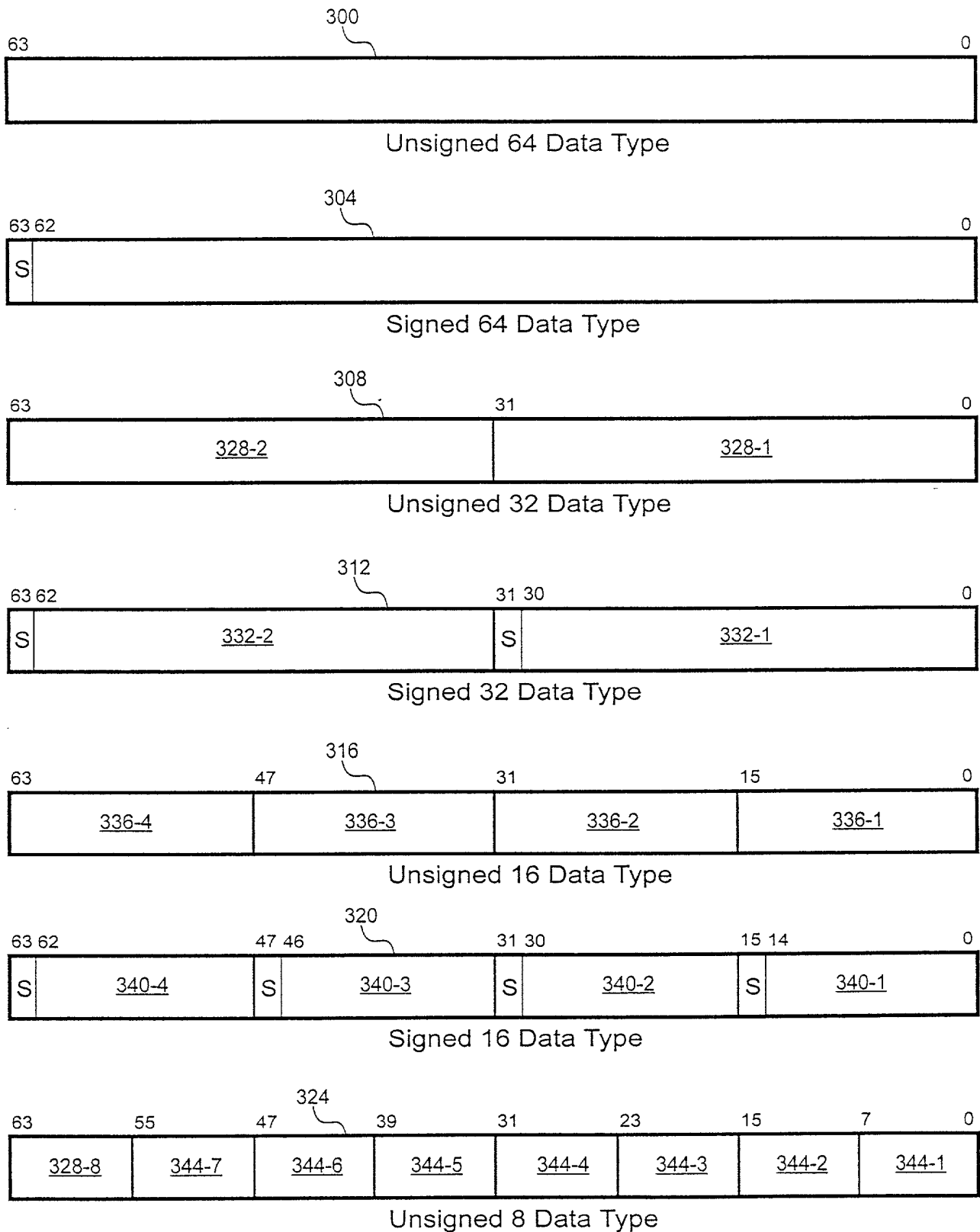


Fig. 3

FIG. 4 is a block diagram of a BSWAP Register Sub-Instruction: 8 bit Operands

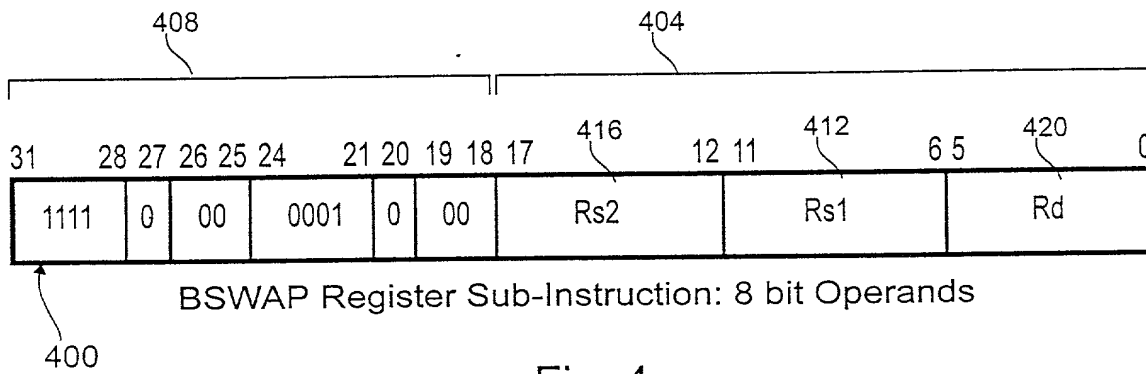


Fig. 4

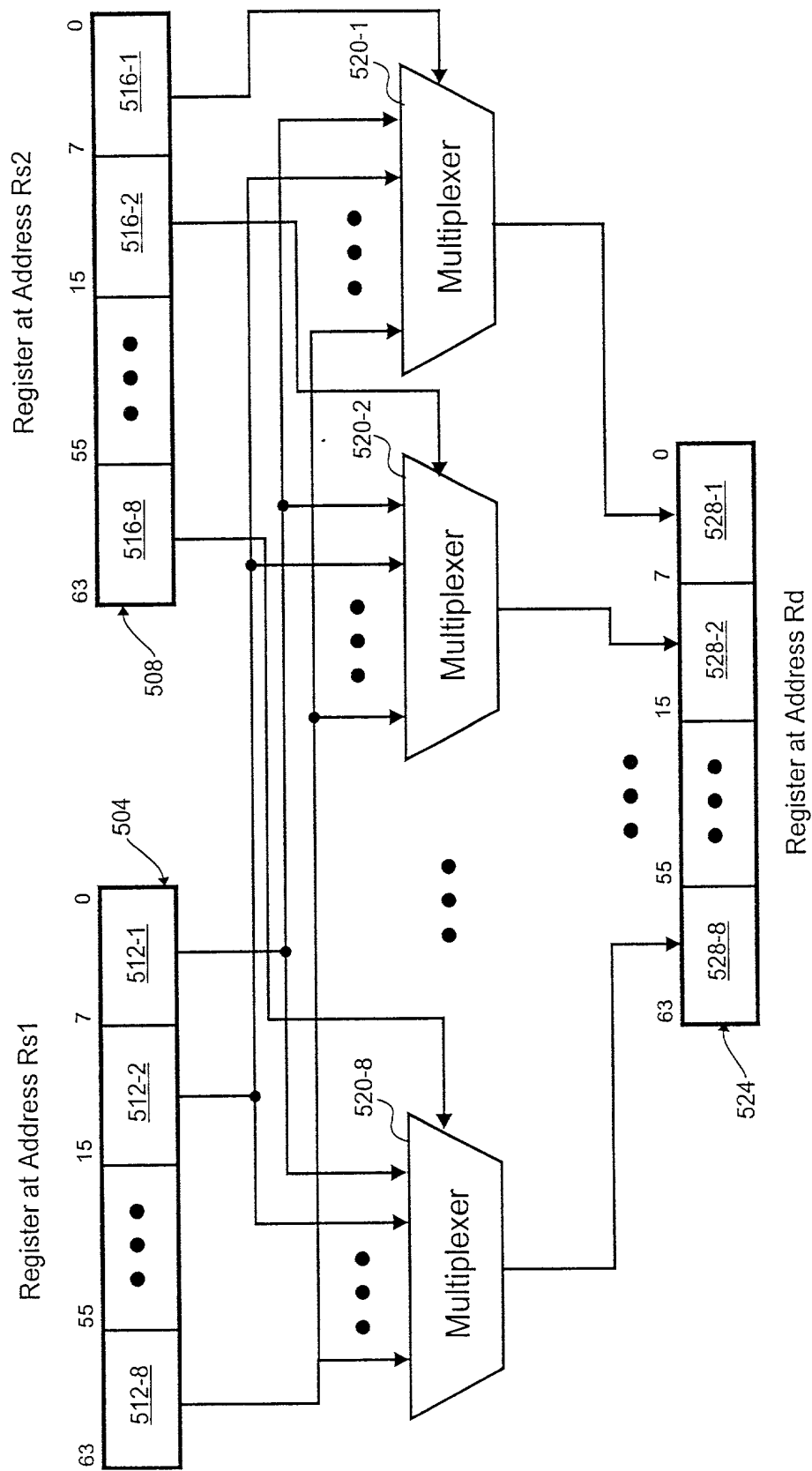


Fig. 5

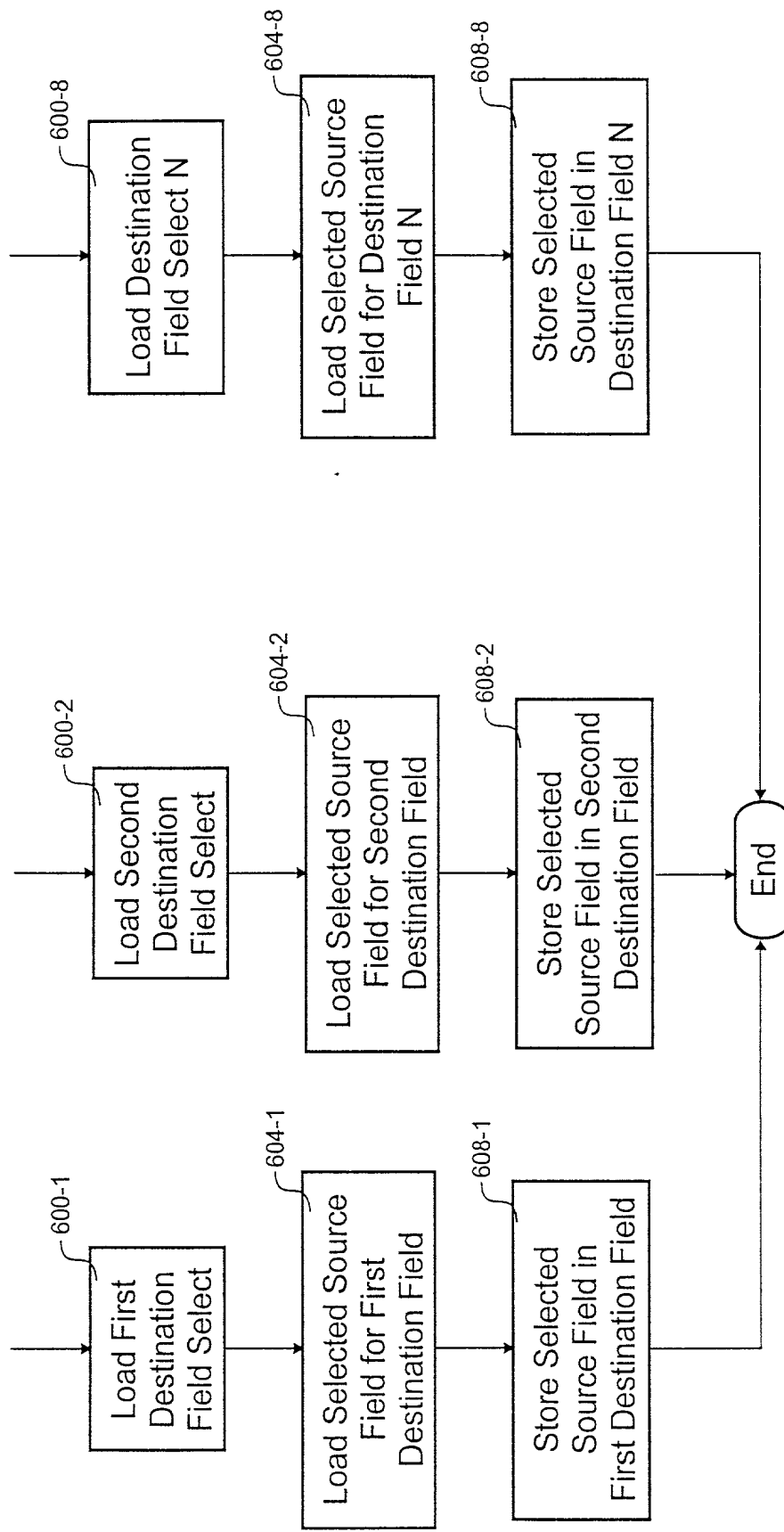


Fig. 6

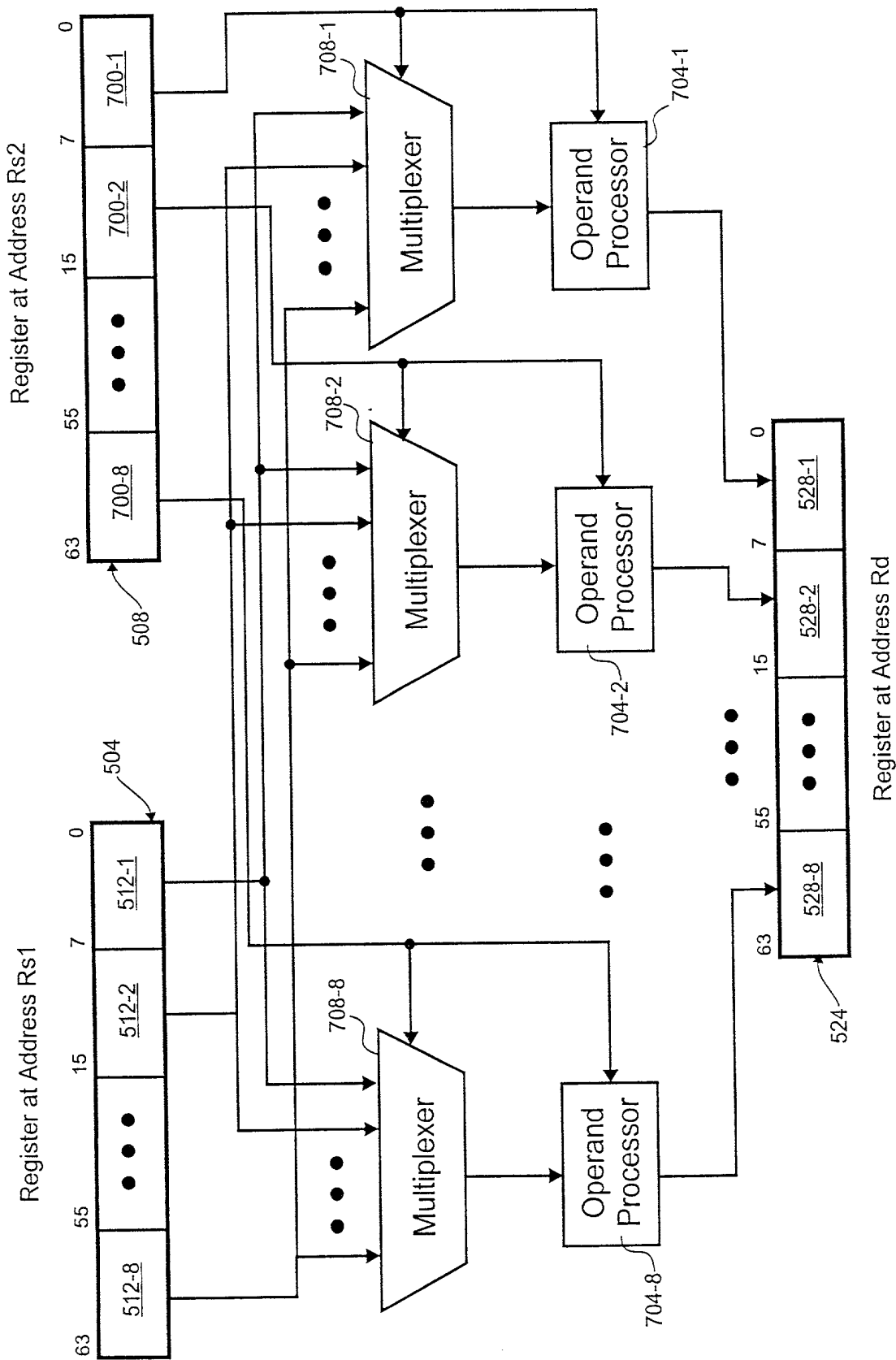


Fig. 7

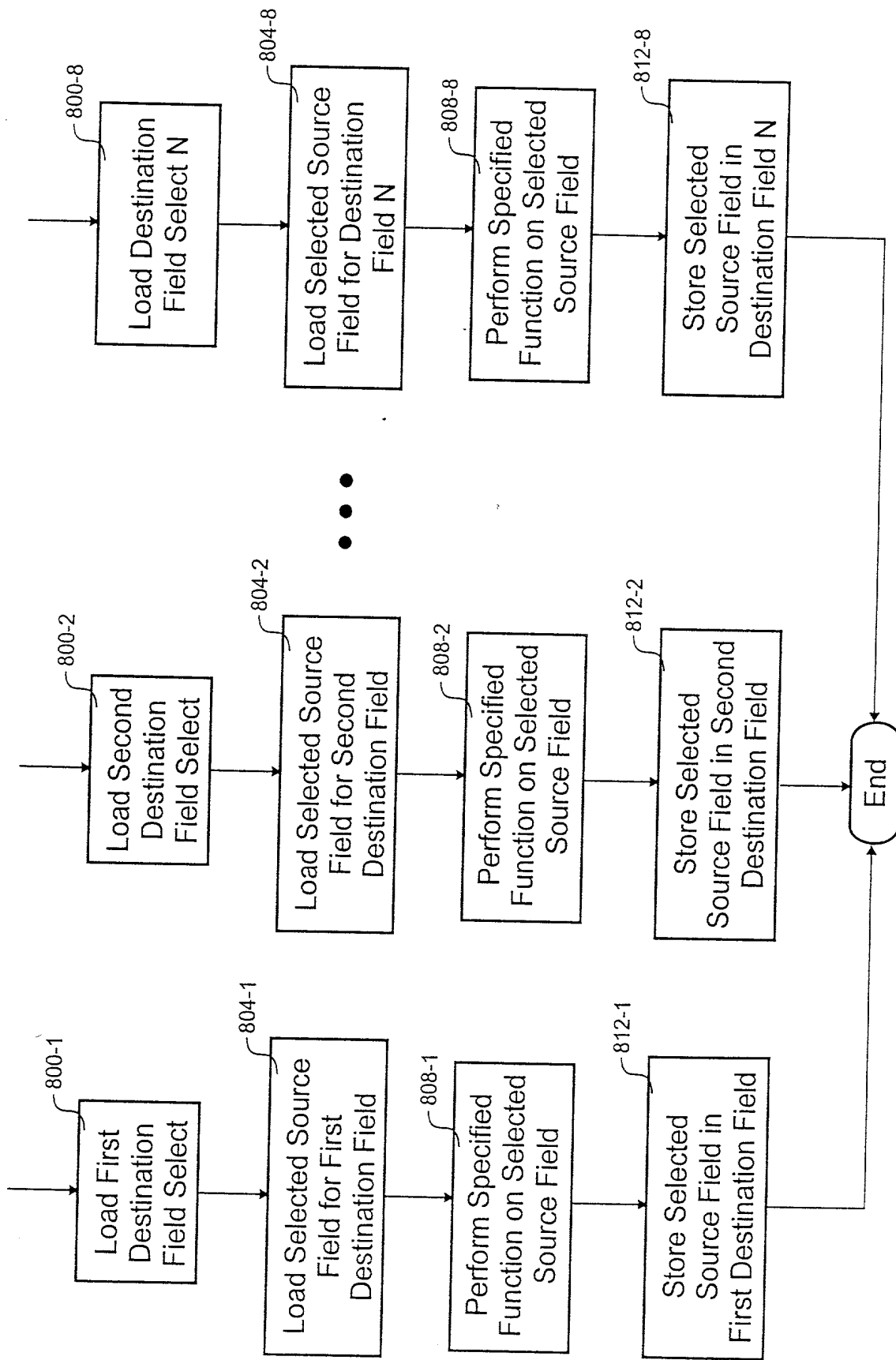


Fig. 8